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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/812,140	03/19/2001	Herbert J. Neuhaus	5740.02 7640		
20686 75	90 01.29.2002				
DORSEY & WHITNEY, LLP SUITE 4700 370 SEVENTEENTH STREET DENVER, CO 80202-5647			EXAMINER		
			WILLIAMS, ALEXANDER O		
DENVER, CO	80202-5647		ART UNIT	PAPER NUMBER	
			2826	-	

DATE MAILED: 01/29/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No		Applicant(s)				
Office Action Summary		09/812,140		NEUHAUS ET AL.				
		Examiner		Art Unit				
		Alexander O Wi		2826				
Period fe	The MAILING DATE of this communication app or Reply	ears on the cove	er sheet with the co	rrespondence address				
A SH THE - Exte after - If th - If NO - Failt - Any	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a reply o period for reply is specified above, the maximum statutory period we reto reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(b).	6(a). In no event, how within the statutory mill apply and will expire cause the application	vever, may a reply be time nimum of thirty (30) days to SIX (6) MONTHS from the become ARANDONED	ly filed will be considered timely. e mailing date of this communication (35.11.5 O. 8.132)	on.			
1) 🗔	Responsive to communication(s) filed on 20 N	lovember 2001						
2a)		s action is non-f	inal					
3)								
Disposition of Claims								
4)[-]	4) Claim(s) 1-92 is/are pending in the application.							
4a) Of the above claim(s) <u>1-20,29-47 and 55-92</u> is/are withdrawn from consideration.								
· —	5) Claim(s) is/are allowed.							
6) Claim(s) <u>21-28 and 48-54</u> is/are rejected.								
7) Claim(s) is/are objected to.								
8)[\]	8) Claim(s) 1-92 are subject to restriction and/or election requirement.							
Applicati	on Papers	·						
9) ☐ The specification is objected to by the Examiner.								
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.								
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.								
If approved, corrected drawings are required in reply to this Office action.								
12)☐ The oath or declaration is objected to by the Examiner.								
Priority u	nder 35 U.S.C. §§ 119 and 120							
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).								
a) ☐ All b) ☐ Some * c) ☐ None of:								
1. Certified copies of the priority documents have been received.								
	2. Certified copies of the priority documents have been received in Application No							
Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.								
14) 🗌 A	14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).							
a) The translation of the foreign language provisional application has been received. 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.								
Attachment(s)								
2) Notice 3) Inform	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO-1449) Paper No(s) <u>5.6</u>	5) 🗍	Interview Summary (P Notice of Informal Pate Other:	TO-413) Paper No(s) ent Application (PTO-152)				
S. Patent and Tra PTO-326 (Rev.		on Summary		Part of Panor No. 1				

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Art Unit: 2826

Serial Number: 09/812140 Attorney's Docket #: 5740.02

Filing Date: 3/19/01;

Applicant: Neuhaus et al.

Examiner: Alexander Williams

Applicant's election with traverse of Group I (device claims 21-28 and 48-54) in Paper No. 10 is acknowledged.

Applicant's arguments beginning on pages 1 and 2 are not found to be persuasive. For example, instant of plating particles into a substrate by a metal plating solution including hard particles in a plating tank, the particles can be placed on the substrate through a CVD process. In the examination of claims Group I (claims 21 to 28 and 48-54) the Examiner would be interested in searching for the final structure of the semiconductor device claimed. In the examination of Group II (claims 1-20, 29-47 and 55-92) the Examiner would be interested in the step claimed to achieve the semiconductor device claimed. Therefore, the two Groups would require a search in different art units and class.

Each of the Groups have searches in different art units and classes that would unduly burden the Examiner to evaluate all claims on their merit at the full time. This is not found persuasive because of the reasons detailed in the last Office action.

The requirement is still deemed proper and is therefore made FINAL.

This application contains claims 1-20, 29-47 and 55-92 drawn to an invention non-elected with traverse in Paper No. 10. A complete response to the final rejection must include cancellation of non-elected claims or other appropriate action (see 37 CFR § 1.144 & MPEP § 821.01).

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

The disclosure is objected to because of the following informalities: the application related applications should be updated.

Appropriate correction is required.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action: A person shall be entitled to a patent unless --

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(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains.

Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(f) or (g) prior art under 35 U.S.C. 103(a).

Claims 21 to 25, 28, and 48 to 50 are rejected under 35 U.S.C. § 102(b) as being anticipated by Zimmer (U.S. Patent # 5,921,856).

For example, in claim 21, Zimmer (figures 1A to 9B) specifically figure 4 show an electrical component assembly comprising: a) a substrate **26** having a plurality of electrical contact sites (inherit) on a surface thereof; and b) a plurality of hard particles **28** positioned on the substrate, such that each of the electrical contact sites has at least one hard particle associated therewith, the hard particles being affixed to the electrical contact sites.

For example, in claim 48, Zimmer (figures 1A to 9B) specifically figure 4 discloses a printed circuit interconnection assembly comprising: a printed circuit board substrate **26** having a plurality of electrical sites (inherit) on a surface thereof; and a

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plurality of hard particles **28** positioned on the substrate, such that each of the plurality of electrical contact sites has at least one hard particle associated therewith, wherein the at least one hard particle is affixed to each electrical contact site.

Claims 21 to 28 and 48 to 54 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Yamagata et al. (U.S. Patent # 6,183,874 B1).

For example, in claim 21, Yamagata et al. (figures 1 to 11) discloses an electrical component assembly comprising: a) a substrate having a plurality of electrical contact sites on a surface thereof; and b) a plurality of hard particles positioned on the substrate, such that each of the electrical contact sites has at least one hard particle associated therewith, the hard particles being affixed to the electrical contact sites (column 10, line 57 to column 11, line 35).

For example, in claim 48, Yamagata et al. (figures 1 to 11) discloses a printed circuit interconnection assembly comprising: a printed circuit board substrate having a plurality of electrical sites on a surface thereof; and a plurality of hard particles positioned on the substrate, such that each of the plurality of electrical contact sites has at least one hard particle associated therewith, wherein the at least one hard particle is affixed to each electrical contact site (column 10, line 57 to column 11, line 35).

As to claims 52 to 54, it would be a matter of design choice to in using the different types of printed circuit board.

Therefore, it would have been obvious to one of ordinary skill in the art to use the teaching of Yamagata et al.'s board to form the device claimed by Applicant for the purpose of providing a substrate for mounting a semiconductor device.

The listed references are cited as of interest to this application, but not applied at this time.

Field of Search	Date
U.S. Class and subclass: 257/778,779,784,786,787,738,737,734,700,701,758	1/25/02
Other Documentation: foreign patents and literature in 257/778,779,784,786,787,738,737,734,700,701,758	1/25/02
Electronic data base(s): U.S. Patents EAST	1/25/02